# PacketExpert<sup>™</sup> 1G - Optical and Ethernet Tester



- Bit Error Rate Testing
- RFC 2544
- Loopback
- ITU-T Y.1564

- Multi-stream traffic generator
- RFC 6349
- Record and playback traffic
- Wirespeed network tap

### **Overview**

PacketExpert<sup>™</sup> is a portable (USB based) Quad Port Ethernet / VLAN / MPLS / IP / UDP tester with 4 Electrical Ethernet ports, 2 of the 4 ports can be Electrical or Optical ports, enabling testing on optical fiber links as well. The electrical ports support 10/100/1000 Mbps, and optical ports support 1000 Mbps using SFP. Each GigE port provides independent Ethernet / VLAN / MPLS / IP / UDP testing at wire speed for applications such as Bit Error Rate Testing, RFC 2544, Loopback, Capture and Playback, Multi-Stream UDP/TCP Traffic Generator and Analyzer, and ITU-T Y.1564 testing for verifying service level agreements.

It truly takes confusion out of Ethernet / IP testing at all protocol layers from Layer1 frames to IP / UDP packets. It can be used as a general purpose Ethernet to IP performance analysis tool for 10 Mbps, 100 Mbps and 1 Gbps Ethernet Local Area Networks (LAN) and Wide Area Networks (WAN).

It can perform Wire speed Bit Error Rate Testing BERT on all ports simultaneously over Framed Ethernet (Layer2), Stacked VLAN (Q-in-Q), Stacked MPLS (Layer 2.5), IP and UDP. It can generate and receive various BER patterns, including PRBS patterns, to properly test the Ethernet to IP link.

With the capability to generate or receive traffic with stacked VLAN (Q-in-Q) and stacked MPLS PacketExpert<sup>™</sup> finds useful in testing a wide range of networks from testing individual links or switches, testing local Ethernet or IP networks (LAN), end-to-end testing of Wide Area Networks (WAN), testing Core or MPLS networks, and much more.

Similar to BERT, RFC 2544 can be done over Framed Ethernet (Layer2), Stacked VLAN (Q-in-Q), Stacked MPLS, IP or UDP. Supported tests such as Throughput, Latency, Frame Loss, and Back to Back tests as specified in RFC 2544 can also be performed. The RFC 2544 test can be done on single or dual electrical or optical ports. In single port, the test can be run on either Port #2 or Port #3 at a time. In dual port, the test can be run on both Port #2 and Port #3 simultaneously.

PacketExpert licensing (PXE100) also supports Loopback testing it includes layer-wise Loopback as well as Smart Loopback testing types on All ports or 2 ports on the hardware unit.

GL also offers other Ethernet / IP tester variants such as -

PacketExpert<sup>™</sup> 1G - mTOP<sup>™</sup> rack enclosure - a higher density 12/24 GigE ports form factor solution.

<u>mTOP<sup>™</sup> Probe with PacketExpert<sup>™</sup> 1G Multi-Port Ethernet</u>– a hardware unit designed for easier portability and convenient for field testing.

For more details, refer to <u>PacketExpert</u><sup>™</sup> webpage.

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### **Main Features**

#### Operations

- Control multiple hardware units from a single GUI, multiplying the number of ports available per system
- Capability of remote operation, and automation using C#, Python scripts and MAPS CLI (client-server) architecture

#### Wire speed BERT

- Traffic options lets technicians to generate Ethernet to IP frames with user-configurable frame length, and frame size with varying traffic rates
- Option to disable Auto-negotiation for 1000Base-T (1000 Mbps Electrical)
- Added additional 1000Base-T Interface parameters when Auto Negotiation is disabled
- Enhanced to support burst mode traffic generation ability
- Supports industry standards PRBS patterns 2<sup>9</sup>-1, 2<sup>11</sup>-1, 2<sup>15</sup>-1, 2<sup>20</sup>-1, 2<sup>23</sup>-1, 2<sup>29</sup>-1, and 2<sup>31</sup>-1, and constant patterns like all ones, all zeroes, alternate ones-zeroes and user-defined test patterns ranging between 1 to 32 bits
- Single as well as constant rate Bit Error and FCS Error Insertion
- Optional sequence number insertion allows to detect out-of-sequence packets and packet loss
- Support for frame lengths from 64 bytes to Jumbo frames (up to 2048 bytes)
- User-defined header parameters for MAC, VLAN, MPLS, IP and UDP layers
- Testing with stacked VLAN (Q-in-Q) and MPLS up to 3 levels

#### Loopback

• Both smart loopback (auto layer detection) and layers-wise loopback capabilities for incoming traffic

#### RFC 2544

- Throughput, back-to-back, latency and frame loss testing supporting uni-directional and bi-directional traffic between ports
- Supports RFC 2544 on single or dual electrical / optical ports
- User-defined configuration parameters such as frame size, trial duration, number of trials, etc.
- Added measured Latency display for both Store/Forward and Bit Forward methods

#### **ExpertSAM**<sup>™</sup>

- ExpertSAM ITU-T Y.1564 Complete validation of Ethernet service-level agreements (SLAs) in a single test
- Supports Service Configuration and Service Performance tests in compliance with ITU-T Y.1564 standard
- Supports multiple services (up to 12 services) with varying performance requirements that meets full load conditions
- KPIs like Information Rate (IR) or Throughput, Frame Loss Ratio (FLR), Frame Transfer Delay (FTD) or Latency, and Frame Delay Variation (FDV) or Jitter, measured simultaneously for multi streams, and Pass/Fail verdict declared

#### **Statistics and Graphs**

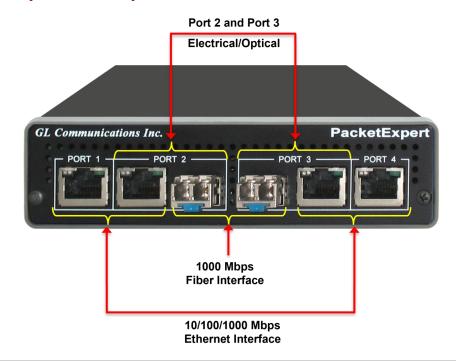
- Detailed test result reports in PDF and CSV file formats
- Detailed frame statistics in tabular format for all the ports
- Provides various vital measurements such as Bit Error Rate, Bit Error Count, Sync Loss Count, Sync Loss Seconds, and Error Free Seconds
- Real time graphical representation of the combined Throughput and Bit Error rate, plotted over time for BERT testing
- Graphs and Statistics for all the RFC 2544 tests
- Link (P2-P3 / P3-P2) statistics and Port (P2, P3) statistics

#### **Optional Applications**

- <u>Capture/Playback</u> Wirespeed Packet Capture, Filter, Drop (for real-time analysis) and Storage (for offline analysis)
- <u>PacketBroker</u> Capture packets non-intrusively with advanced features like Filters, Aggregation, Packet Modification, and Output traffic
- <u>Multi-Stream UDP/TCP Traffic Generator and Analyzer</u> Generates and analyzes up to 12 user defined streams of varying packet length traffic

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# Portable PacketExpert™ 1G Specifications

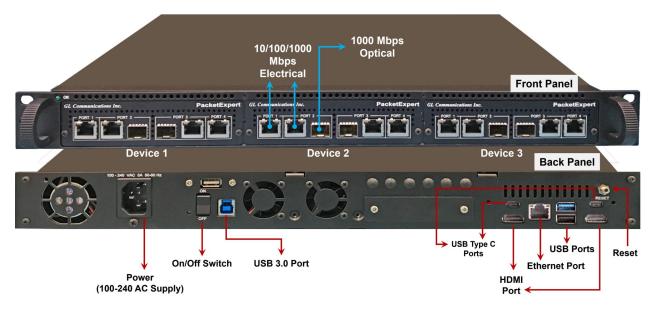


Interfaces	<ul> <li>2 x 10 / 100 / 1000 Base-T Electrical only</li> <li>2 x 100 Base-FX Optical only</li> <li>2 x 1000 Base-X Optical OR 10/100/1000 Base-T Electrical</li> <li>Single Mode or Multi Mode Fiber SFP support with LC connector</li> </ul>
Protocols	RFC 2544 compliance
Bus Interface	• USB 2.0 or USB 3.0
Power	+12 Volts (Medical Grade), 3 Amps
Temperature	<ul> <li>Operating Temperature: +5 to +40C</li> <li>Non-Operating Temperature: -30 to +60C</li> </ul>
Humidity	<ul> <li>Operating Humidity: 0% to 80% RH</li> <li>Non-Operating Humidity: 0% to 95% RH</li> </ul>
Altitude	<ul> <li>Operating Altitude: Up to 10,000 feet</li> <li>Non-Operating Altitude: Up to 50,000 feet</li> </ul>
Physical Specification	<ul> <li>Length: 8.45 in. (214.63 mm)</li> <li>Width: 5.55 in. (140.97 mm)</li> <li>Height: 1.60 in (40.64 mm)</li> <li>Weight: 1.66 lbs. (0.75 kg)</li> </ul>



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## mTOP<sup>™</sup> PacketExpert<sup>™</sup> 1G Rack Specifications



#### Figure: 1U mTOP<sup>™</sup> Rack Based 1G Hardware Unit (3 PXE100s)

Interfaces	12 Total Ethernet Ports (HD-PacketExpert-12)
	<ul> <li>mTOP<sup>™</sup> System (embedded SBC, 3x PXE100)</li> <li>PacketExpert<sup>™</sup> 1G (PXE100) interfaces - <ul> <li>6x 1000 Base-X Optical OR 10/100/1000 Base-T Electrical</li> <li>6x 100 Base-FX Optical only</li> <li>6x (10/100/1000) Base-T Electrical</li> </ul> </li> <li>24 Total Ethernet Ports (HD-PacketExpert-24)</li> </ul>
	<ul> <li>mTOP<sup>™</sup> System (embedded SBC, 6x PXE100)</li> <li>PacketExpert<sup>™</sup> 1G (PXE100) interfaces -</li> <li>12x 1000 Base-X Optical OR 10/100/1000 Base-T Electrical</li> <li>12x (10/100/1000) Base-T Electrical</li> </ul>
SBC Specifications	<ul> <li>Intel Core i3 or optional i7 NUC Equivalent,</li> <li>Windows® 11 64-bit Pro Operating System</li> <li>USB 3.0 and USB 2.0 Ports, ATX Power Supply</li> <li>USB Type C Ports, Ethernet 2.5GigE port</li> <li>256 GB Hard drive, 8G Memory (Min)</li> <li>Two HDMI ports</li> </ul>
External Dimension	<ul> <li>Length: 16 Inches</li> <li>Width: 19 Inches</li> <li>Height: 2x 1U mTOP<sup>™</sup> (HD-PacketExpert-24) or 1U mTOP<sup>™</sup> (HD-PacketExpert-12)</li> </ul>
Power Supply	ATX Power Supply
Order Information	<ul> <li>PXE100 - PacketExpert<sup>™</sup> Options</li> <li>MT001/MT001E (1U)</li> <li>MT001+MT002/ MT001E+MT002 (Stacked 1U)</li> </ul>

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## mTOP<sup>™</sup> 1G Probe Specifications



Figure: mTOP<sup>™</sup> Probe with 1G Hardware Unit + SBC

Interfaces	<ul> <li>4x Total Ethernet ports</li> <li>2 x 10/100/1000 Base-T Electrical only</li> <li>2 x 100 Base-FX Optical only</li> <li>2 x 1000 Base-X Optical OR 10/100/1000 Base-T Electrical</li> <li>Single Mode or Multi Mode Fiber SFP support with LC connector</li> </ul>	
SBC Specifications	<ul> <li>Intel Core i3 or optional i7 NUC Equivalent,</li> <li>Windows® 11 64-bit Pro Operating System</li> <li>USB 3.0 and USB 2.0 Ports, 12/3A Power Supply</li> <li>USB Type C Ports, Ethernet 2.5GigE port</li> <li>256 GB Hard drive, 8G Memory (Min)</li> <li>Two HDMI ports</li> </ul>	
External Dimension	<ul> <li>Length: 10.4 inches</li> <li>Height: 3 inches</li> <li>Width: 8.4 inches</li> </ul>	
Power Supply	• 12 Volts (Medical Grade), 3 Amps	
Order Information	<ul><li>PXE100</li><li>MT005/MT005E</li></ul>	

### Pelican Carry On Case





### Wire-speed BER Testing with Traffic Generation

Wire-speed BERT measures Bit Error Rate on Framed Ethernet, Stacked VLAN, Stacked MPLS (Layer 2.5), IP and UDP layers. Supports generating various PRBS patterns such as 2<sup>9</sup>-1, 2<sup>11</sup>-1, 2<sup>15</sup>-1, 2<sup>20</sup>-1, 2<sup>23</sup>-1, 2<sup>29</sup>-1, 2<sup>31</sup>-1, including constant patterns such as All Ones, All Zeroes, Alternate Ones-Zeroes and user-defined test patterns ranging from 1 bit to 32 bits. Selection of optional sequence number insertion allows detection of out-of-sequence packets and packet loss. The Rx Config includes an option to process received packets for FCS errors. BERT can run simultaneously on all ports or can be combined with Loopback to run BERT. In addition, the PacketExpert<sup>™</sup> also allows sending traffic of specific frame length and rate.

Tx Config	Rx Config
Port Selection Port 2 💽 🗹 Tx Rx coupled settings	Port Selection Port 2 💽 Tx Rx coupled settings
Layer MAC MPLS IP UDP BERT Traffic	Layer MAC MPLS IP UDP BERT
BERT Configuration Ber Pattern User Defined	BERT Configuration Ber Pattern User Defined
31 User Defined Pattern 0	31 User Defined Pattern 0
11111111 V Invert	1111111111 V Invert
All Ones All Zeros 7F	All Ones All Zeros 3ff
Length 7 💌	Enable Sequence Number      Process FCS Error Frames

Figure: BERT payload with various PRBS patterns

### Wire speed BERT Results (with LEDs and Graph)

PacketExpert<sup>™</sup> analyzes the received BER pattern and provides various vital measurements such as Bit Error Rate, Bit Error Count, Bit Error Seconds, Sync Loss Count, Sync Loss Seconds, Error Free Seconds, No Rx Data Count/Seconds, and Bert Status. It also gives a real time graphical representation of the Throughput and Bit Error rate, plotted over time as shown in figure below. Supports Test Report Generation in PDF and CSV file formats.

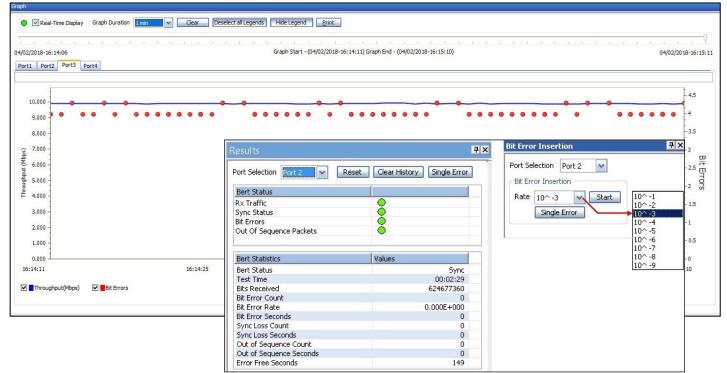


Figure: BERT Results (with LEDs) and Graph

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### Link Status and Configuration

Hardware Interface details are displayed independently per port. It includes:

- Hardware MAC address
- Link status
- Current Operating Mode (Electrical / Optical)
- Auto Negotiated Status and Link Speed
- Duplex Mode
- Flow Control

Users can plug in to the port either Electrical or Optical operating mode. Also, users can opt for Auto negotiated speed or force the speed to one of 10 / 100 /1000 Mbps. Default IP address settings for the port can be configured. Send Pause Frame feature to send user-defined Quanta of pause frames at a time manually on each port independently.

Details		Settings		
Hardware MA	C address	Interface Type	Electrical	~
00-21-C2-00-	09-В0	Link Speed	Auto	~
Status Link	•	_ IP Address	Disable Au	ito Negotiatio
Interface Type	Electrical	IP Addres	ss 192 . 168	3.1.11
Auto-Negotiation Status	Complete	Subnet Ma	sk 255 . 25	5.255.0
Speed	1000 Mbps	Default Gatewa	av 192 . 168	3.1.1
Duplex Mode	Full Duplex		·	
Flow Control	Enabled		Apply	

Figure: Interface Dialog & Link Status

## Packet Configuration Stacked VLAN (Q- in-Q), MPLS

BERT and RFC2544 testing over Ethernet and MPLS allows configuration of various layer parameters.

PacketExpert<sup>™</sup> supports up to 3 level stacked VLANs (Q-in-Q) headers and stacked MPLS headers. For each VLAN tag, user can specify the VLAN Type Field, VLAN Id and Priority. User can specify MPLS Label ID, Class of service (CoS) bits and TTL field for each MPLS level (MPLS #1, MPLS #2 and MPLS #3).

Tx Config	
	Tx Config
Port Selection Port 2 🔽 Tx Rx coupled settings	Port Selection Port 2 🔽 🔽 Tx Rx coupled settings
Layer MAC MPLS IP UDP BERT Traffic	For Coopied Settings
	Layer MAC MPLS IP UDP BERT Traffic
Source MAC Address 00-21-c2-00-04-b9	SS MPLS Stack - 3
Destination MAC Address 00-21-c2-00-04-ba Resolve	MPLS #1
Length/Type 88-47 MPLS	Label 1234 CoS 1 TTL 128
VLAN Stack 3	
VLAN#1 Type 81-00 VLAN#1 Type	Label 5678 CoS 5 TTL 128
VLAN#2 Type 88-A8 V ID 22 Priority 3	MPL5 #3
VLAN#3 Type 91-00 💌 ID 333 Priority 7	Label 9011 Cos 7 TTL 255

Figure: Stacked VLAN and MPLS Configurations

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### Packet Configuration over IP and UDP

PacketExpert<sup>™</sup> allows users to configure Ethernet /MPLS / IP / UDP header parameters, including stacked VLAN IDs, stacked MPLS labels, Frame Size, and Rate.

Users can edit various packet header parameters at Layer2 (framed Ethernet), Layer 2.5 (stacked MPLS), Layer3 (IP) and Layer4 (UDP) for both BERT and RFC 2544 testing.

[Layer 3] - IP - allows Source and Destination IP Addresses. Users can configure various IP header fields like TOS, TTL, Protocol, Header Checksum, and Identification field.

[Layer 4] - UDP - allows Source and Destination UDP ports to be defined. User can allow hardware to calculate / verify checksum or provide a fixed value.

rt Selection Por		coupled settings	
yer MAC M	PLS IP UDP	BERT Traffic	
P Selection - IPv	4		
Src IP Address	Interface Settin	ngs	
IP Addres	s 192.168.1.	12	
Subnet Mas	k 255.255.255.	0	
Default Gatewa	y 192.168.1.	1 Enable	
Dest IP Address	192.168.1.13	3 Ping	
TOS/DS	0	Header Checksum 00-00	Auto
ΠL	128	Identification 00-00	Auto
Protocol	17	JDP	

Tx Config					
Port Selection Po	ort 2	<b>~</b>	Tx Rx 🛛	coupled	settings
Layer MAC I	MPLS	IP	UDP	BERT	Traffic
Source Port	20000				
Destination Port	30000				
Checksum	00-00		🗹 AI	uto	

Figure: IP and UDP Layers Configuration

### **Tx and Rx Frame Statistics**

Detailed statistics per port are provided. In addition to statistics like Frame Count, Frame Rate, Link Utilization, others are provided based on various categories like Frame Type (Unicast/Broadcast/Multicast, VLAN), frame lengths (64, 65-127, 1024-1518, Oversized, Undersized), Protocol Type (IPv4, IPv6, UDP, TCP, ICMP, IGRP, etc.). VLAN Statistics (per Stack position), MPLS Statistics (per stack position) are also displayed for the configured stacks.

Statistics			
Port Selection Port 1 Reset			
Description	Tx	Rx	^
Total Frames	1 364 381	1 364 508	
Valid Frames	1 362 819	1 362 718	
Number Of Bytes	2 068 759 242		
Link Utilisation	100.00		
DataRate(Mbps)	987.00	986.99	
Frame Rate(Frames\Second)	81 275	81 274	
indire nate(indires (second)	01 270	01 27 1	
Broadcast Frames	0	0	
Multicast Frames	0	1 364 380	
Control Frames	0	0	
VLAN Frames	0	0	
Pause Frames	0	0	
Wrong Opcode Frames	0	0	
64 Byte Length Frames	0	0	≣
65-127 Byte Length Frames	0	0	
128-255 Byte Length Frames	0	0	
256-511 Byte Length Frames	0	0	
512-1023 Byte Length Frames	0	0	
1024-1518 Byte Length Frames	1 364 381	1 364 508	
Oversized Frames	0	0	
Undersized Frames	-	0	
FCS Error Frames	-	0	
Non Test Frames	-	0	
Non Test VLAN Frames	-	0	
Non Test MPLS Frames	-	0	
1 Level Stacked VLAN Frames	-	0	
2 Level Stacked VLAN Frames	-	0	
3 Level Stacked VLAN Frames	-	0	
1 Level Stacked MPLS Frames	-	0	
2 Level Stacked MPLS Frames	-	0	
3 Level Stacked MPLS Frames	-	0	
IP Checksum Errors	-	0	
IPv4 Packets	-	1 388 695	
IPv6 Packets	-	0	
IP Non Test Packets	-	0	*
IP Non Test Packets	- A anal Dant Cta	-	۷

**Figure: General Port Statistics** 



## Loopback Testing

PacketExpert<sup>™</sup> has All ports / 2 ports Loopback capability. PacketExpert<sup>™</sup> supports Layer-wise Loopback as well as Smart Loopback. Supported Loopback types are -

- Smart Loopback Analyzes incoming traffic, automatically detects and swaps Source and Destination MAC, IP, and UDP addresses before sending back the packet
- Layer-wise Loopback Swaps Source and destination MAC/IP/UDP addresses before sending back the packet and loops back the incoming packets

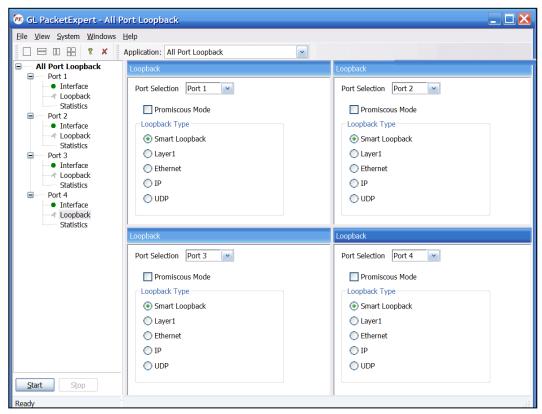


Figure: Loopback Testing

## **Command Line Interface (CLI)**

PacketExpert<sup>™</sup> is enhanced to support Command Line Interface (CLI) with additional CXE100 license to access all the functionalities remotely using , Python client ,C# client and MAPS<sup>™</sup> CLI Server.

The CLI supports all the PacketExpert<sup>™</sup> test modules including All Port Bert, Bert Loopback, All Port Loopback, RFC 2544, Record / Playback, PacketBroker, ExpertSAM<sup>™</sup>, and Multi-Stream Traffic Generation and Analysis.



## **Buyer's Guide**

Item No	Product Description
<u>PXE100</u>	PacketExpert <sup>™</sup> 1G
<u>CXE100</u>	CLI support for PXE100
Item No	Related Hardware
<u>PXE104</u>	PacketExpert™ - SA (4 ports) 1G
<u>PXE112</u>	PacketExpert™ - SA (12 Ports) 1G
<u>PXE124</u>	PacketExpert™ - SA (24 Ports) 1G
Item No	Related Software
<u>PXE105</u>	Wire speed Record/Playback 1G
<u>PXE107</u>	PacketBroker™ 1G
<u>PXE108</u>	Multi-Stream UDP/TCP Traffic Generator and Analyzer

**Note:** PCs which include GL hardware/software require Intel or AMD processors for compliance.

For more details, refer to <u>PacketExpert<sup>™</sup> 1G</u> webpage.



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